OP550, OP552, OP555, OP560, OP565, OP750, OP755 Series

Features:

- Wide receiving angle
- Four sensitivity ranges
- Side-looking package
- Ideal for space-limited applications
- Ideal for PCBoard mounting
- Choice of clear, opaque or blue-tinted package

Description:

Electronics OP552 OP550 OP555 OP560 OP565 **OP750 OP755 OP770 OP775**

OP550, OP552, OP555, OP750, OP755, OP770 and OP775 series consists of a NPN silicon phototransistor molded in an epoxy package with a wide receiving angle that provides relatively even reception over a large area. The **OP750**, **OP755**, **OP770** and **OP775** have additional circuitry to enhance the operation of the device for stray light levels.

OP560 and OP565 series consists of a NPN silicon photodarlington transistor molded in an epoxy package with a wide receiving angle that provides relatively even reception over a large area.

The side-looking package design allows easy PCBoard mounting of slotted optical switches or optical interrupt detectors.

The OP550, OP560, OP750 and OP770 devices have an external lens in a clear epoxy package.

The **OP552** device has an integral lens in an opaque plastic package that is optically transparent to infrared light but opaque to visible wavelengths. This feature allows the device to be used under high ambient light conditions – or anywhere external light sources could interfere with the intended sensing application (visible light immunity).

The OP555, OP565, OP755 and OP775 devices have an internal lens in a blue-tinted package. The lensing effect of this package allows an acceptance half-angle of 28° when measured from the optical axis to the half-power point. These devices are 100% production tested using infrared light for close correlation with OPTEK's GaAs and GaAIAs emitters. All of these sensors are mechanically and spectrally matched to the OP140, OP142, OP145, OP240 and OP245 series of infrared emitting diodes.

Please refer to Application Bulletins 208 and 210 for additional design information and reliability (degradation) data. For custom versions please contact your OPTEK representative.

Applications:

- Applications requiring wide receiving angle
- Applications requiring PCBoard mounting
- Space-limited applications
- Optical switches
- Optical interrupt detectors
- Optical encoders
- Non-contact position sensing
- Machine automation

OF	,	
55 = Phototransistor		
56 = Photodarlington		
Phototransistor with Rac		I

- **77** = Phototransistor with C_{CE}
- A = Highest sensitivity level B = Sensitivity Level with Min. Max. C = Middle Sensitivity Level D = Lowest Sensitivity Level 0 = Extended Lens - Clear Package
- 2 = Extended Lens Blue Tinted Package
- 5 = Integral Lens Blue Tinted Package

Available Part Numbers								
OP550A	OP552A	OP555A	OP560A	OP565A	OP750A	OP755A	OP770A	OP775A
OP550B	OP552B	OP555B	OP560B	OP565B	OP750B	OP755B	OP770B	OP775B
OP550C	OP552C	OP555C	OP560C	OP565C	OP750C	OP755C	OP770C	OP775C
OP550D	OP552D	OP555D			OP750D	OP755D	OP770D	OP775D

General Note

RoHS

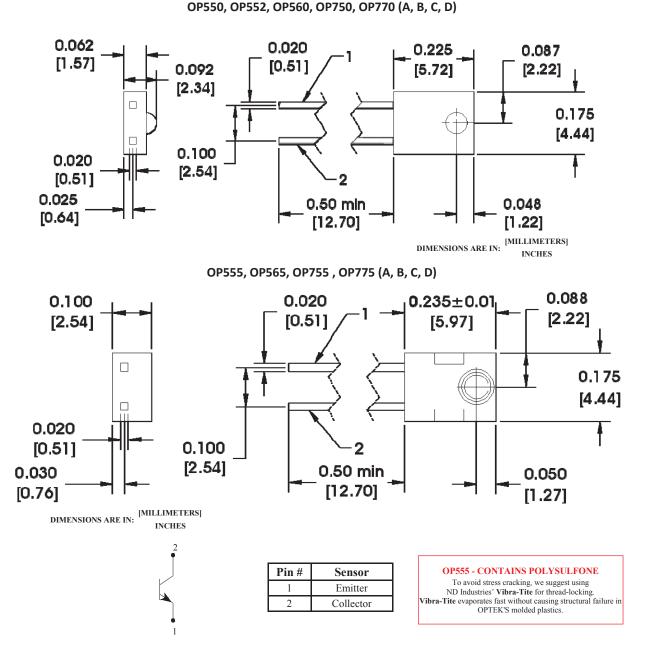
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- 75 = Phototransistor with R_{BE}

OP550, OP552, OP555, OP560, OP565, OP750, OP755 Series





Notes:

- 1. RMA flux is recommended. Duration can be extended to 10 seconds maximum when flow soldering. A maximum 20 grams force may be applied to the leads when soldering.
- 2. For OP550, OP560, OP555 and OP565, derate linearly 1.33 mW/° C above 25° C. For OP552, derate linearly 1.25 mW/° C above 25° C.
- 3. For all phototransistors in this series, the light source is an unfiltered GaAs LED with a peak emission wavelength of 935 nm. For OP550 and OP555 only, a radiometric intensity level that varies less than 10% over the entire lens surface of the phototransistor being tested applies.
- 4. To calculate typical collector dark current in μ A, use the formula $I_{CEO}=10^{(0.040 T_A^{-3.4})}$, where T_A is ambient temperature in °C.

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OP550, OP552, OP555, OP560, OP565, OP750, OP755 Series



Electrical Specifications

Storage Temperature Range	-40° C to +100°
Operating Temperature Range	
OP550, OP555, OP560, OP565, OP750, OP755	-40° C to +100°
OP552	-40° C to +85°
Collector-Emitter Voltage	
OP550, OP552, OP555, OP560, OP750, OP755	30
OP565	15
Emitter-Collector Voltage	5
Lead Soldering Temperature [1/16 inch (1.6 mm) from case for 5 seconds with soldering iron]	260° C
Power Dissipation	
OP550, OP552, OP555, OP560, OP565, OP755	100 mW
OP750	200 mW

OP550, OP552, OP555, OP560, OP565, OP750, OP755 Series



Electrical Specifications							
Electrical Characteristics (T _A = 25° C unless otherwise noted)							
SYMBOL	PARAMETER	MIN	ТҮР	MAX	UNITS	TEST CONDITIONS	
	On-State Collector Current 2.55 - - OP550A, OP552A, OP555A 2.55 - - OP550B, OP552B, OP555B 1.30 - 4.70 OP550C, OP552C, OP555C 0.25 - 2.40 OP550D, OP552D, OP555D 0.25 - -		V _{CE} = 5.0 V, E _E = 1.0 mW/cm ²⁽³⁾				
	OP560A, OP565A OP560B, OP565B OP560C, OP565C	6.6 3.3 1.1	- -	- 9.8 -	· mA	V_{CE} = 2.0 V, E_{E} = 0.1 mW/cm ²⁽³⁾	
·	OP750A OP750B OP750C OP750D	2.25 1.50 0.85 0.85		7.00 4.20 2.80 7.00		V _{CE} = 5.0 V, E _E = 1.0 mW/cm ²⁽³⁾	
I _{C(ON)}	OP755A OP755B OP755C OP755D	1.80 1.20 0.70 0.70	- - -	5.50 3.40 2.25 5.50			
-	OP770A OP770B OP770C OP770D	2.25 1.50 0.85 0.85	- - -	7.00 4.20 2.80 7.00			
	OP775A OP775B OP775C OP775D	1.80 1.20 0.70 0.70		5.50 3.40 2.25 5.50			
Ι _c /Δ Τ	Relative I _c Charge with Temperature	-	1.00	-	%/°C	$V_{CE} = 5.0 \text{ V}, \text{ E}_{\text{E}} = 1.0 \text{ mW/cm}^2, \lambda = 935 \text{ nm}$	
I _{CEO}	Collector-Dark Current	-	-	100	nA	$V_{CE} = 10.0 \text{ V}, \text{ E}_{\text{E}} = 0^{(4)}$	
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage OP550, OP552, OP555, OP750, OP755, OP770, OP775	30	-	-	V	$I_{\rm C} = 100 \ \mu \text{A}, \ E_{\rm E} = 0^{(4)}$	
Vienter	OP560, OP565 Emitter-Collector Breakdown Voltage	15 5.0	-	-	v	$I_{c} = 1 \text{ mA}, E_{E} = 0^{(4)}$ $I_{E} = 100 \ \mu\text{A}$	
V _{(BR)ECO}	Collector-Emitter Saturation Voltage OP550, OP552, OP555, OP750, OP755, OP770, OP775 OP560, OP565	-	-	0.40	V	$I_c = 100 \ \mu\text{A}$ $I_c = 100 \ \mu\text{A}$, $E_e = 1.0 \ \text{mW/cm}^{2(3)}$ $I_c = 0.4 \ \text{mA}$, $E_e = 0.1 \ \text{mW/cm}^{2(3)}$	

See page 2 for Notes

General Note

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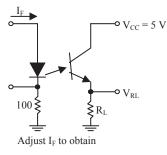
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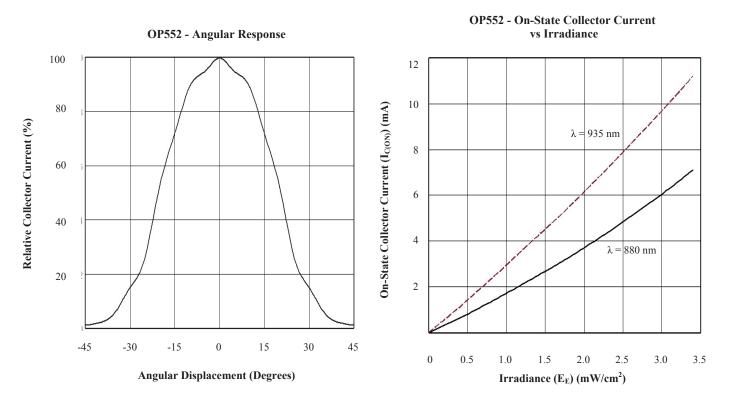
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Performance

Switching Test Circuit



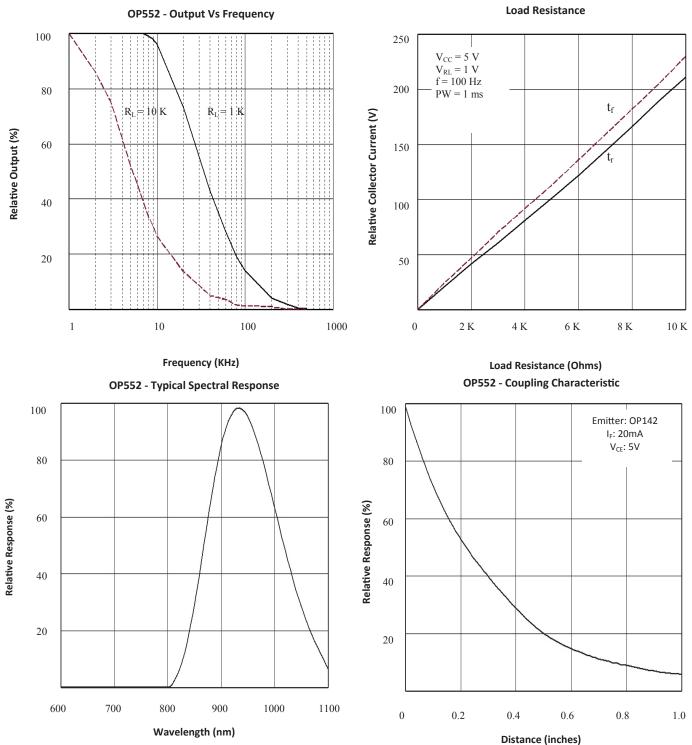


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Performance



OP552 - Rise and Fall Time vs

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